

Catalog No.	Rev.	Description
1756sc-IF8H	2.9	<ul style="list-style-type: none"> <li>Fixed to send the correct number of preambles.</li> <li>Enhanced to alternate short address 0 with other addresses for faster initialization.</li> <li>Fixed to keep completed pass through responses when configuration changed.</li> <li>Enhanced to no longer block command 6.</li> <li>Fixed to reduce the number of clear configuration changed bit commands sent when the configuration is being changed.</li> </ul>
1756sc-OF8H	2.9	<ul style="list-style-type: none"> <li>Fixed to send the correct number of preambles.</li> <li>Enhanced to alternate short address 0 with other addresses for faster initialization.</li> <li>Fixed to keep completed pass through responses when configuration changed.</li> <li>Enhanced to no longer block command 6.</li> <li>Fixed to reduce the number of clear configuration changed bit commands sent when the configuration is being changed.</li> </ul>
1756sc-IF8H	2.5	Fix software to be HART version 5 compliant when decoding HART command #50.
1756sc-OF8H	2.5	Fix software to be HART version 5 compliant when decoding HART command #50.
1756sc-IF8H	2.3	Addition of user selectable unconnected messaging scheduling. This allows the user to determine the rate that the pass-through message queue is serviced. Service rate may be set higher when used with CONNECTS gateway to optimize performance
1756sc-OF8H	2.3	Addition of user selectable unconnected messaging scheduling. This allows the user to determine the rate that the pass-through message queue is serviced. Service rate may be set higher when used with CONNECTS gateway to optimize performance
1756sc-IF8u	2.4	CJC data can freeze or drop out affecting data quality Allow use of GSV to retrieve I/O fault
1756sc-IF8u	2.3	Add a short circuit condition detection to the CJC sensor. If the data ready signal is not received from the ADC within the timeout for the filter chosen, reset the ADC. If the situation remains for 3 samples, issue a HW fault and stop processing.
1756sc-IF8u	2.1	RSLogix V16 intermittent connection with extended RPI settings fixed. Add-on profile major and minor revision support added.
1756sc-IF8u	1.33	When operating in 3-wire RTD/Resistance mode, the lead resistance correction computation was incorrect. The algorithm introduces a substantial error when the sensor resistance changes. This error persists until the next lead resistance measurement occurs.